



The IEEE Computer Society
Technical Committee on
VLSI

Technical Co-Sponsor



3rd IEEE INTERNATIONAL SYMPOSIUM ON NANOELECTRONIC and INFORMATION SYSTEMS

(DEC 18-20, 2017 @ Bhopal, INDIA)

CALL FOR STUDENT RESEARCH FORUM PAPERS

<http://www.ieee-inis.org>



IEEE-iNIS 2017 will host a student research forum (SRF) as its integral part for the benefit of research scholars and graduate students. The submission deadline is the same as specified for the regular paper submissions. All the accepted student research forum (SRF) papers will be published in the IEEE-iNIS 2017 proceedings to be published by IEEE-CS-CPS.

The **primary objective of IEEE-iNIS** is to provide a platform for both hardware and software researchers to interact under one umbrella for further development of efficient and secure information processing technologies. Efficient and secure data sensing, storage, and processing play pivotal roles in current information age. The state-of-the-art nanoelectronic technology based hardware systems cater to the needs of efficient sensing, storage, and computing. At the same time, efficient algorithms and software used for faster analysis and retrieval of desired information are becoming increasingly important. Big data which are large, complex data sets, are now a part of the Internet world. Storing and processing needs of the enormous amount of structured and unstructured data are getting increasingly challenging. At the same time, Internet of Things (IoT) and cyber-physical systems (CPS) have been evolving with simultaneous development of hardware and software and span across everyday consumer electronics. The performance and efficiency of the present as well as the future generations of computing and information processing systems are largely dependent upon advances in both hardware and software. IEEE-iNIS has been initiated as a sponsored meeting of Technical Committee on VLSI, IEEE-CS (<http://www.ieee-tcvlsi.org/>) that endorses a league of successful meetings including ASAP, ISVLSI, and ARITH, are now presented as "Sister Conferences".

CONTRIBUTIONS ARE SOUGHT IN THE FOLLOWING AREAS:

Nanoelectronic VLSI and Sensor Systems (NVS) Energy-Efficient, Reliable VLSI Systems (ERS) Hardware/Software for Internet of Things and Consumer Electronics (IoT) Hardware for Secure Information Processing (SIP) Hardware/Software Solutions for Big Data (SBD) Cyber Physical Systems and Social Networks (CSN) .

Detailed discussions of these tracks are provided in IEEE-iNIS website. IEEE-iNIS 2017 will have a **student research forum** as its integral part.

PAPER SUBMISSION DEADLINES:

Extended Hard Deadline: ~~July 28, 2017~~ (Extended to August 15, 2017)

Acceptance Notification: September 23, 2017

Submission of Final Version: October 10, 2017

IEEE-iNIS 2017 proceedings will be published by IEEE-CS conference publications services (CPS). Authors should submit their original unpublished work of maximum 4 pages using IEEE-CS double-column conference format-template. Manuscripts in PDF format with author information (optional) should be submitted to the "Student Research Forum" track using the following link: <https://easychair.org/conferences/?conf=inis2017>.

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